



ATTENTION:

**OFFICE OF INITIAL
PATENT EXAMINATION'S
FILING RECEIPT CORRECTIONS**

Attorney Docket No.: 8956-000105/US

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicants: Kyu-Nam LIM et al.

Application No.: 10/660,568

Conf. No.: 5304

Group No.: 2818

Filed: September 12, 2003

For: SEMICONDUCTOR MEMORY DEVICE HAVING HIERARCHICAL
STRUCTURE OF DATA INPUT/OUTPUT.

LETTER REQUESTING CORRECTED OFFICIAL FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 9, 2003

Sir:

The Official Filing Receipt mailed December 08, 2003 (copy attached herewith) does not reflect the correct spelling of an applicant's name of the subject application.

The Applicant's name should be listed as follows:

Kye Hyun Kyung

The applicant respectfully requests issuance of a corrected Official Filing Receipt.

Changes to be made are indicated in red on the attached copy of the Official Filing Receipt.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment of Deposit Account No. 08-0750 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

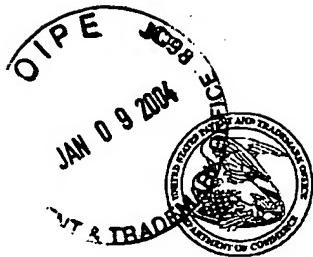
HARNESS, DICKEY & PIERCE, P.L.C

By _____
Johan A. Castellano, Reg. No. 35,094

P.O. Box 8910
Reston, VA 20195
(703) 668-8000

JAC:jos

Attachment: A copy of the Official Filing Receipt with requested correction marked in red ink.

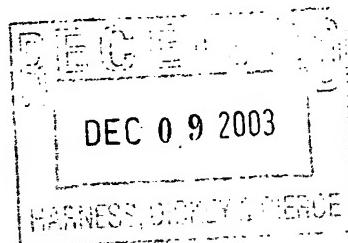


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APPL NO.	FILING OR 371 (c) DATE	ART. UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/660,568	09/12/2003	2818	750	8956-000105/US	5	7	3

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HARNESS, DICKEY & PIERCE, P.L.C.
P.O. BOX 8910
RESTON, VA 20195



CONFIRMATION NO. 5304

FILING RECEIPT



OC000000011433322

Date Mailed: 12/08/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Kyu-Nam Lim, Yongin-city, KOREA, REPUBLIC OF;
Kyo-Hyun Kyung, Yongin-city, KOREA, REPUBLIC OF;

KHE

Domestic Priority data as claimed by applicant**Foreign Applications**

REPUBLIC OF KOREA 2002-67405 11/01/2002

If Required, Foreign Filing License Granted: 12/05/2003

Projected Publication Date: 05/06/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor memory device having hierarchical structure of data input/output line and precharge method thereof

Preliminary Class

**LICENSE FOR FOREIGN FILING UNDER
Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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